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October 30 2008

TO: Examiner: LEE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10921.0427USWO

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Total pages, including cover letter: 3

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Title of Document: **RESPONSE TO RESTRICTION REQUIREMENT (2 PAGES)**

Applicant: TANIMURA ET AL.
Serial No.: 10/593674
App. Filed: September 21, 2006
Group Art No.: 2832
Conf. No.: 2861

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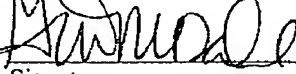
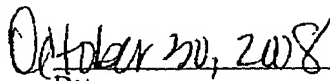
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Name: Douglas P. Mueller

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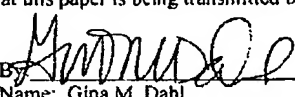
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S/N 10/593,674

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TANIMURA ET AL.	Examiner:	LEE
Serial No.:	10/593,674	Group Art Unit:	2832
Filed:	SEPTEMBER 21, 2006	Docket No.:	10921.0427USWO
Title:	CHIP RESISTOR AND MANUFACTURING METHOD THEREOF		

CERTIFICATE UNDER 37 CFR 1.6(d): I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on October 30, 2008.

By 
Name: Gina M. Dahl

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants elect Group I (claims 1 to 5) in response to the Restriction Requirement of October 8, 2008.

An early and favorable action on the merits is requested.

S/N 10/593,674

PATENT

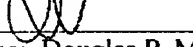
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No. 50-3478.

Respectfully submitted,

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Date: October 30, 2008

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